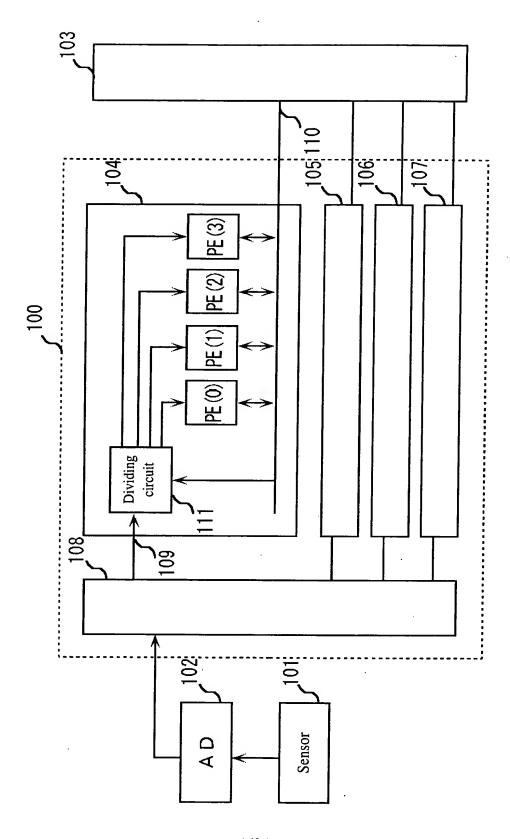
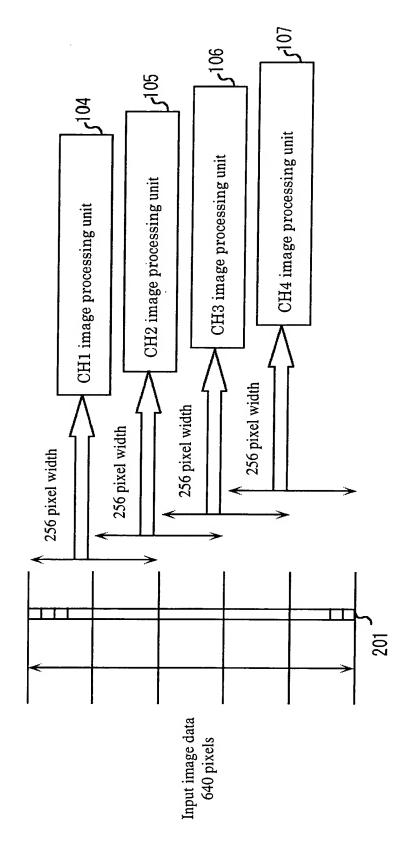
App No.: Not Yet Assigned Docket No.: H6808.0041/P04
Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION
TOOL Docket No.: H6808.0041/P041

Sheet 1 of 21



App No.: Not Yet Assigned Docket No.: H6808.0041/P04
Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION
TOOL

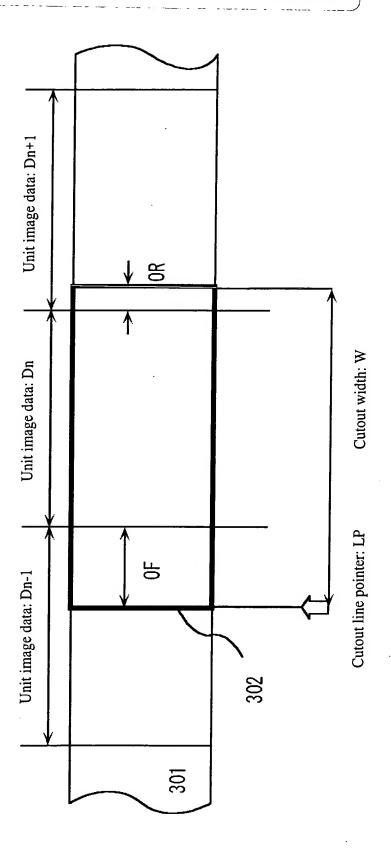
Sheet 2 of 21



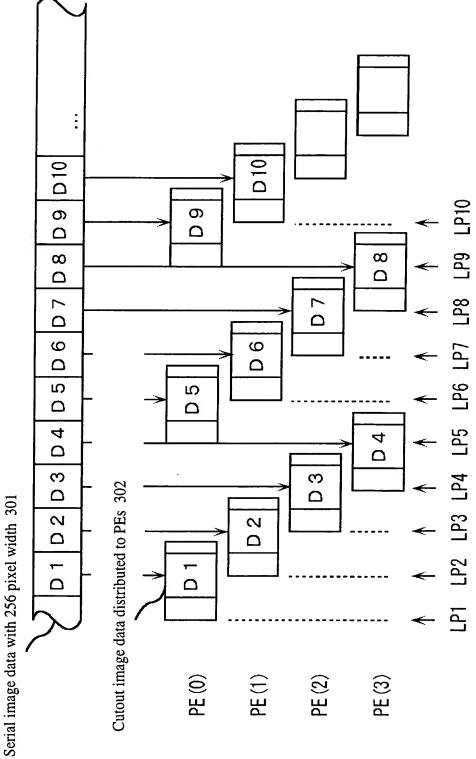
App No.: Not Yet Assigned Docket No.: H6808.0041/P04 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 3 of 21



3/21

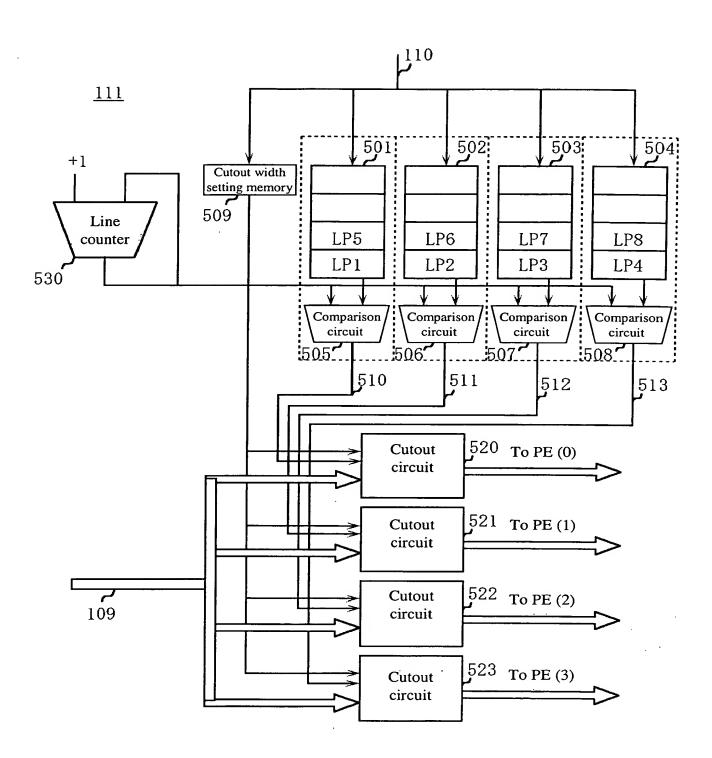


App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 5 of 21

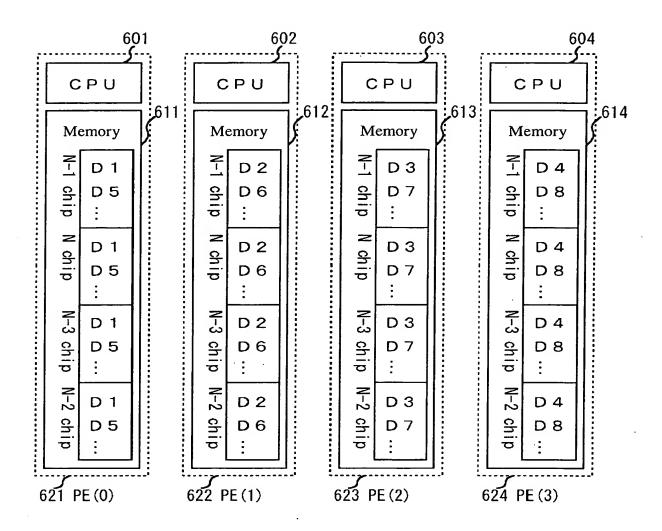
FIG. 5



App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

FIG. 6

Sheet 6 of 21



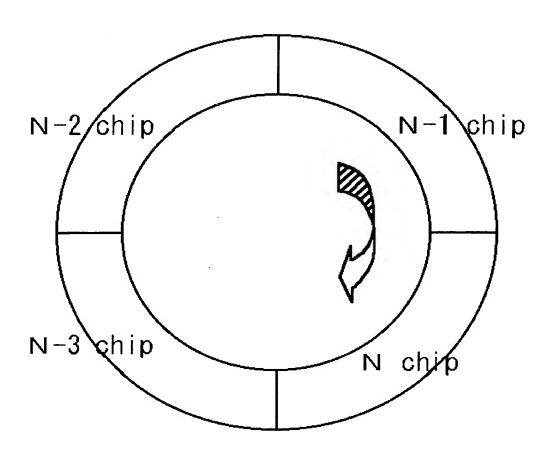
App No.: Not Yet Assigned Docket No.: H6808.0041/P04 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION Docket No.: H6808.0041/P041

TOOL

Sheet 7 of 21

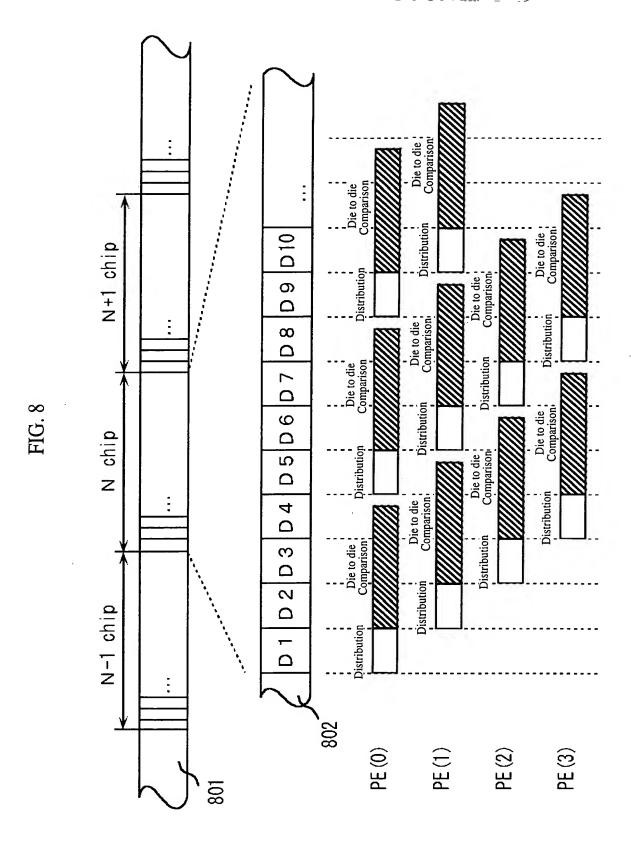
FIG. 7



App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 8 of 21

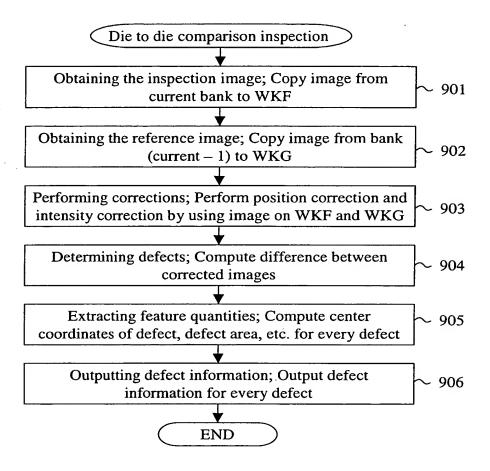


App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 9 of 21

FIG. 9



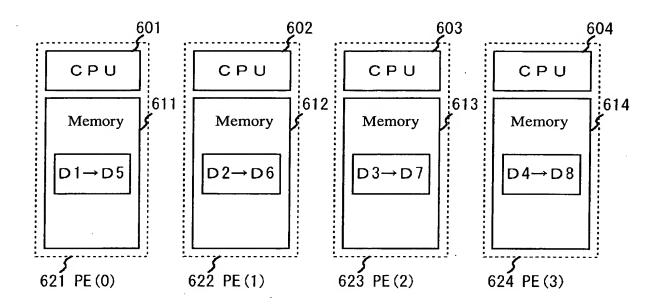
App No.: Not Yet Assigned Inventor: Michio Nakano et al. Docket No.: H6808.0041/P041

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION

TOOL

Sheet 10 of 21

FIG. 10



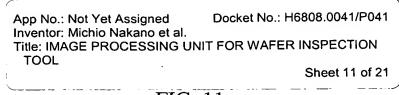
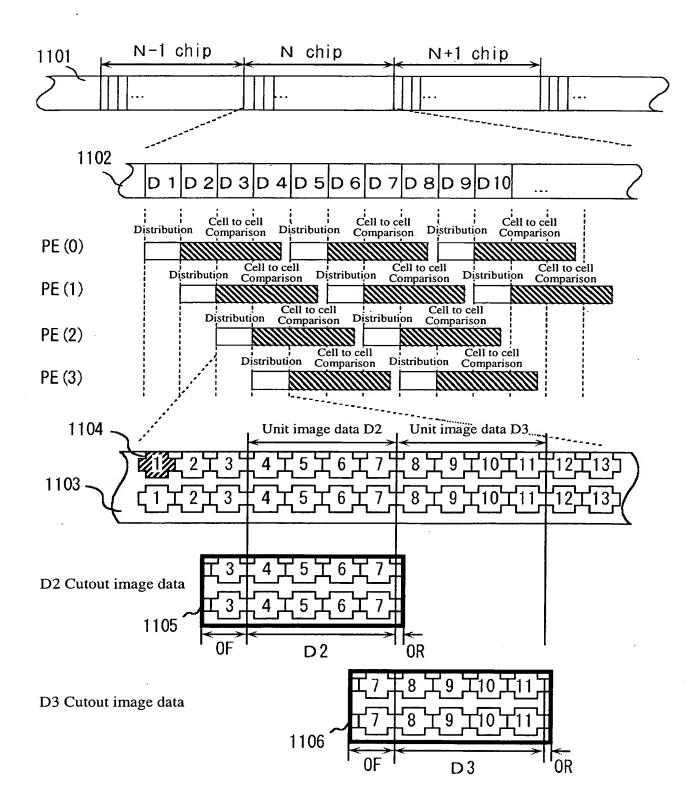
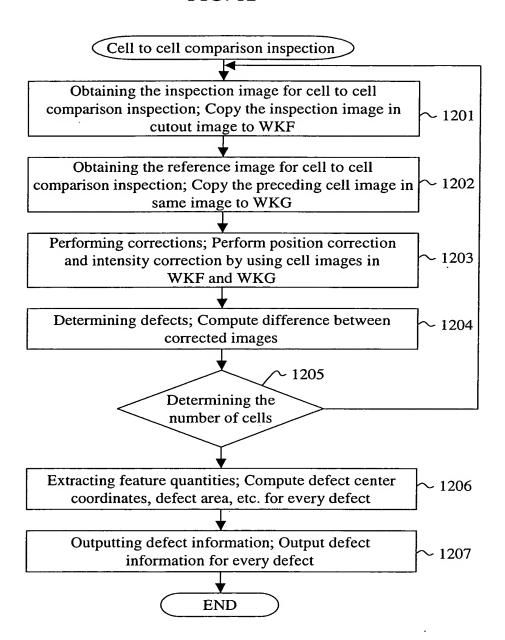


FIG. 11



App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL
Sheet 12 of 21

FIG. 12

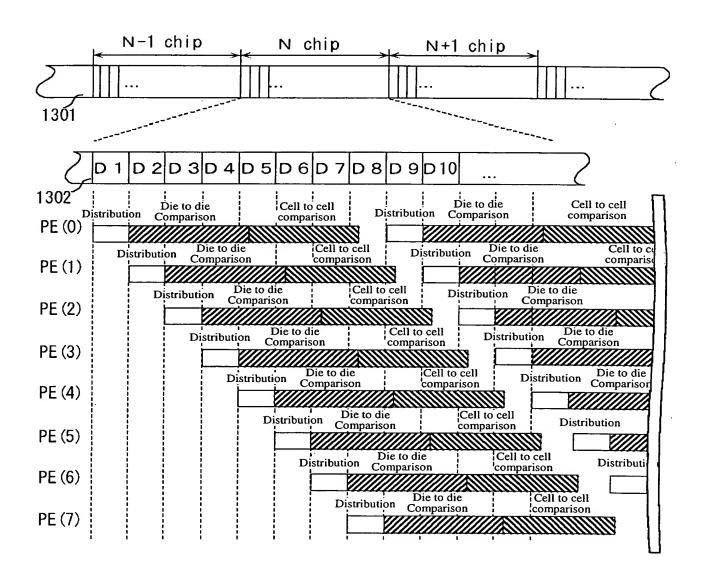


App No.: Not Yet Assigned Docket No.: H6808.0041/P041 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 13 of 21

FIG. 13



Docket No.: H6808.0041/P041 App No.: Not Yet Assigned Inventor: Michio Nakano et al. Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION Sheet 14 of 21 FIG. 14 Cell-to-cell and die-to-die hybrid comparison inspection Obtaining the inspection image; Copy image from current 1401 bank to WKF Obtaining the reference image; Copy image from Bank .1402 (current - 1) to WKG Performing corrections; Perform position correction and .1403

intensity correction by using image data in WKF and WKG Determining defects; Compute difference between corrected

\_1404 images

Obtaining inspection image; Copy inspection cell region in image data to WKF

Obtaining the reference image; Copy cell region in same \_1406 image data to WKG

Perform corrections; Perform position correction and \_1407 intensity correction by using cell images in WKF and WKG

Determining defects; Compute difference between corrected \_1408 cell images

\_1409

1410 ـ

Determine the number of cells

Extracting feature quantities; Compute defect center coordinates, defect area, etc. for every defect

Outputting defect information; Output defect information  $\sim 1411$ for every defect

**END** 

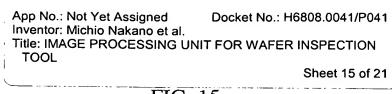
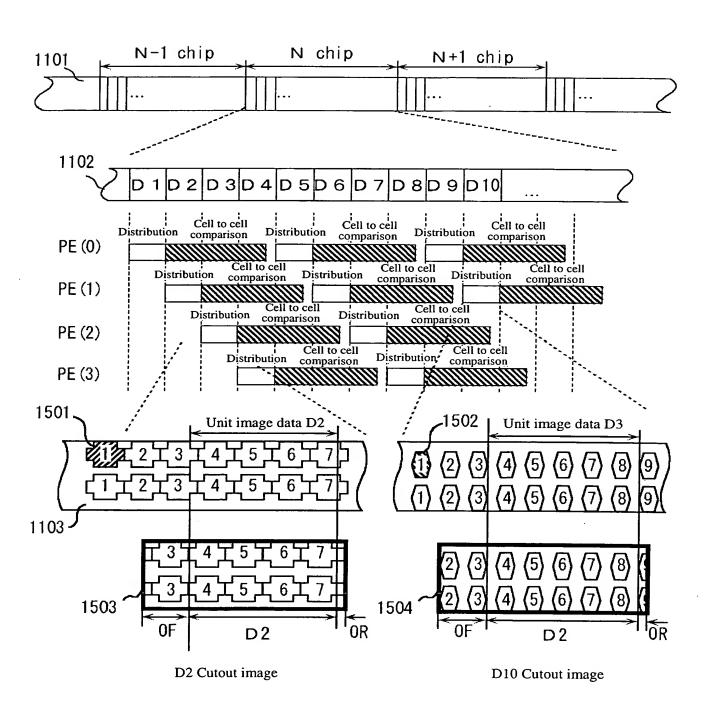


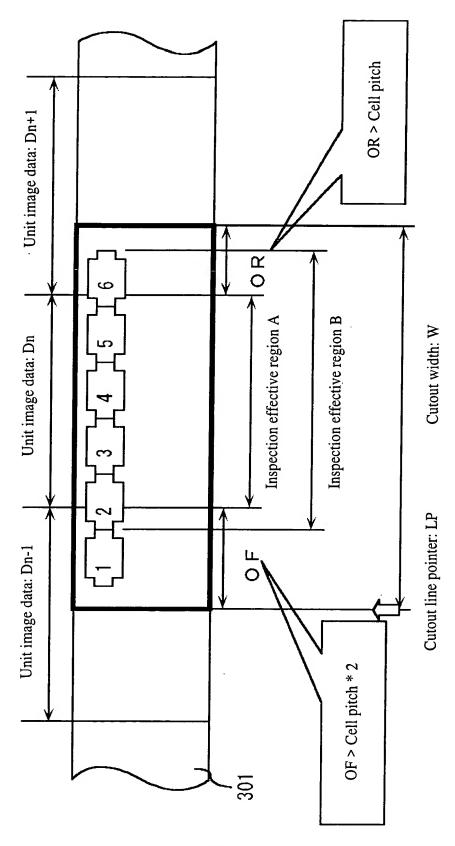
FIG. 15



App No.: Not Yet Assigned Docket No.: H6808.0041/P04 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION

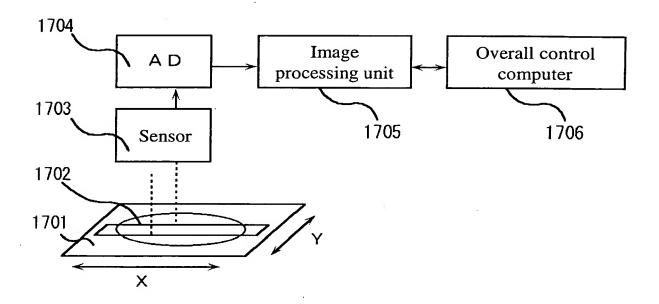
Sheet 16 of 21



16/21

App No.: Not Yet Assigned Docket No.: H6808.0041/P041
Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION
TOOL Sheet 17 of 21

FIG. 17



Sheet 18 of 21

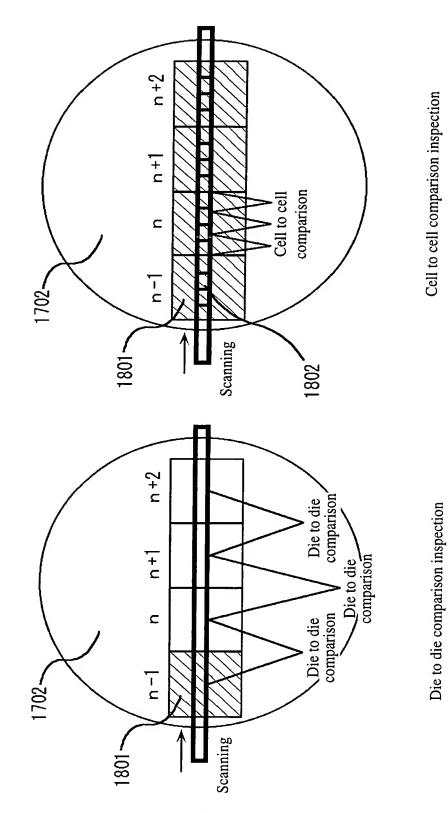
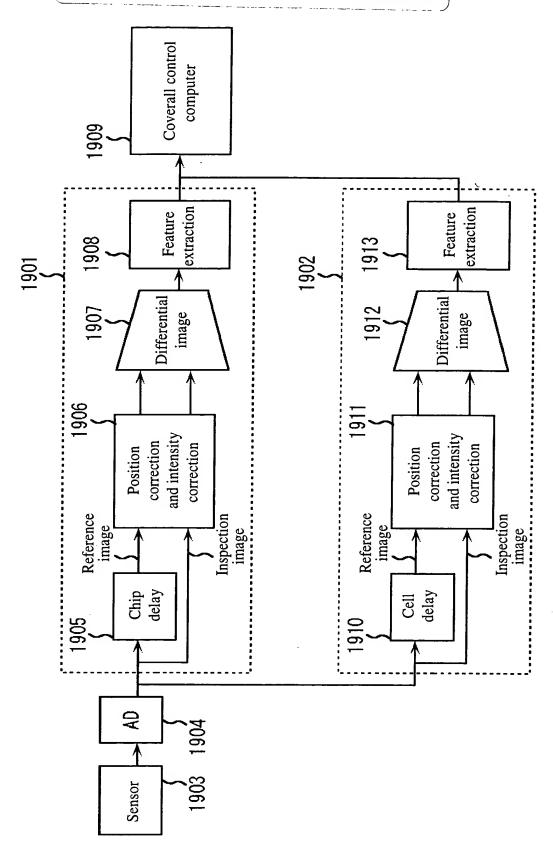


FIG. 18 A

App No.: Not Yet Assigned Docket No.: H6808.0041/P04 Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION TOOL

Sheet 19 of 21



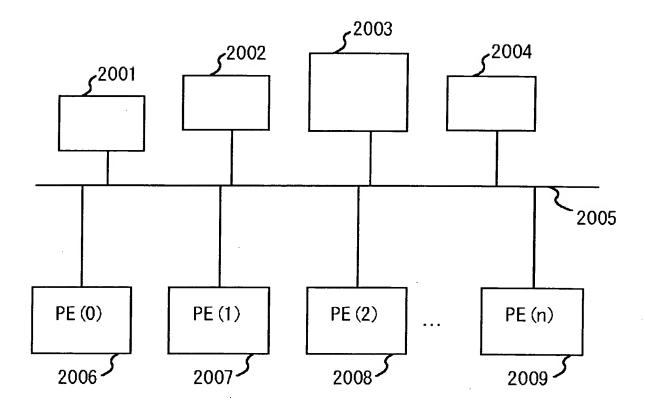
Docket No.: H6808.0041/P041

App No.: Not Yet Assigned Docket No.: H6808.0041/P04
Inventor: Michio Nakano et al.
Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION

TOOL

Sheet 20 of 21

FIG. 20



App No.: Not Yet Assigned

Docket No.: H6808.0041/P041

Inventor: Michio Nakano et al.

Title: IMAGE PROCESSING UNIT FOR WAFER INSPECTION

TOOL

Sheet 21 of 21

FIG. 21

